STOCK# / ISSUE CLASSIFICATION

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PATENT NUMBER

U.S. UTILITY Patent Application

O.I.P.E.

PATENT DATE

APPLICATION NO.	CONT/PRIOR	CLASS	SUBCLASS	ART UNIT	EXAMINER
09/917913	D F	365	230.03	2818	YOHA

APPLICANTS

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236 DIV

TITLE

Semiconductor memory device having a DRAM and a SRAM on a single chip

PTO-2040
12/99

ISSUING CLASSIFICATION

ORIGINAL

CLASS	SUBCLASS

INTERNATIONAL CLASSIFICATION

CROSS REFERENCE(S)

CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)

☐ Continued on Issue Slip Inside File Jacket

☐ TERMINAL DISCLAIMER

☐ The term of this patent subsequent to _____ (date) has been disclaimed.

☐ The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____

☐ The terminal ___ months of this patent have been disclaimed.

DRAWINGS			CLAIMS ALLOWED	
Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.

_____ (Assistant Examiner) _____ (Date)

_____ (Primary Examiner) _____ (Date)

_____ (Legal Instruments Examiner) _____ (Date)

NOTICE OF ALLOWANCE MAILED

ISSUE FEE

Amount Due	Date Paid

ISSUE BATCH NUMBER

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